

AF/2787

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Krishna Rangasayee et al.

Serial No.: 08/707,694

Title: PROGRAMMABLE LOGIC DEVICE HAVING AN
INTEGRATED PHASE LOCK LOOP

Filed: September 4, 1996

Attorney Docket No.: 0325.00063

Examiner: Butler, D.

Art Unit: 2787

In Response To: Office Action mailed December 23, 2000

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231, on February 13, 2001.

By: Mary Donna Berkley
Mary Donna Berkley

Enter
DmB
2-26-01
Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT AFTER FINAL

Sir:

In response to the Office Action mailed December 23, 2000, please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

D